| 30 | PIN | SIMM | at | the | computer. |
|----|-----|------|----|-----|-----------|
|----|-----|------|----|-----|-----------|

| n:- | | Barandakian | IIgs m | emory expansion slot |
|-----|-------|-----------------------|--------|----------------------|
| Pin | Name | Description | | |
| 1 | VCC | +5 VDC | 1 | GND |
| 2 | /CAS | Column Address Strobe | 2 | +5V |
| 3 | DQO | Data 0 | 3 | FRA9 FRA8 |
| 4 | AO | Address 0 | 4 5 | D2 |
| 5 | A1 | Address 1 | 6 | FRA6 |
| 6 | DQ1 | Data 1 | 7 | FRA3 |
| 7 | A2 | Address 2 | 8 | FRA4 |
| | | | 9 | FRA5 |
| 8 | A3 | Address 3 | 10 | FRA7 |
| 9 | GND | Ground | 11 | +5V |
| 10 | DQ2 | Data 2 | 12 | FR/W |
| 11 | Ad | Address 4 | 13 | FRA0 FRA2 |
| 12 | A.5 | Address 5 | 15 | FRA1 |
| 13 | DQ3 | Data 3 | 16 | D7 |
| 14 | A6 | Address 6 | 17 | /CCAS |
| 15 | A7 | Address 7 | 18 | CROWO |
| 16 | DQ4 | Data 4 | 19 | CROW1 |
| | | | 20 | /CROMSEL |
| 17 | A8 | Address 8 | 21 | +5V |
| 18 | A.9 | Address 9 | 22 | GND |
| 19 | A10 | Address 10 | 23 | +5V |
| 20 | DQ5 | Data 5 | 25 | DO |
| 21 | /WE | Write Enable | 26 | /CSEL |
| 22 | GND | Ground | 27 | MSIZE |
| 23 | DQ6 | Data 6 | 28 | D6 |
| 24 | A11 | Address 11 | 29 | D4 |
| 25 | DQ7 | Data 7 | 30 | D5 |
| 26 | QP | Data Parity Out | 31 | PHI2 ABORT |
| | | | 32 | D3 |
| 27 | /RAS | Row Address Strobe | 34 | GND |
| 28 | /CASP | Something Parity ???? | 35 | /CRAS |
| 29 | DP | Data Parity In | 36 | D1 |
| 30 | VCC | +5 VDC | 37 | A10 |
| | | | 38 | A11 |
| | | | 39 | A12 |
| | | | 40 | A13 |
| | | | 41 | A14 A15 |
| | | | 42 | - Francisco |

Here is the pinout of the

+5V

GND

43

44